



Docket No.: 1614.1210

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re the Application of:

Teruhiko KAMIGATA, et al.

Serial No. 10/053,707

Group Art Unit: 2454

Confirmation No. 7916

Filed: January 24, 2002

Examiner: Larry D. Donaghue

For: INSTRUCTION PROCESSING METHOD FOR VERIFYING BASIC INSTRUCTION
ARRANGEMENT IN VLIW INSTRUCTION FOR VARIABLE LENGTH VLIW
PROCESSOR

AMENDMENT UNDER 37 C.F.R. 1.312

MAIL STOP: ISSUE FEE

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Notice of Allowance mailed September 4, 2009. The above-referenced application has been allowed. The Issue Fee has not yet been paid.

The following amendments are respectfully submitted. Entry of the amendments to the claims is respectfully requested.